

REMARKS

The claims are claims 1 to 3, 5, 6, 8, 10 to 13, 15, 16, 18 and 20.

Claims 1, 10, 11 and 20 are amended. Claims 21, 22, 23, 25, 26, 28 and 30 are newly canceled.

Claims 1, 11 and 21 are amended to change "based on the a vector table" to "based on a vector table" in response to the objection of paragraph 5 of the OFFICE ACTION.

Claim 10 is amended to change "selected from the group comprise" to "selected from the group comprising" in response to the object of paragraph 6 of the OFFICE ACTION.

Claims 1, 11 and 21 are amended to change "the plurality of functions comprise command functions that request and grant the identifier to the algorithm" will be interpreted as "the plurality of functions comprise command functions that request and grant an identifier to the algorithm" as suggested in paragraph 8 of the OFFICE ACTION.

Claim 1 is amended to change "a function that is optimized for the configured settings" to "a function that is optimized for configured settings of the logical resource" as suggested in paragraph 9 of the OFFICE ACTION.

Claims 1, 10, 11 and 20 are amended to change "the most recently configured settings of the logical resource" to "most recently configured settings of the logical resource" as suggested in paragraph 10 of the OFFICE ACTION.

Claims 11 and 20 have been amended to delete "high-performance." As pointed out in paragraph 11 of the OFFICE ACTION this phrase has no defined meaning and thus is unclear. This phrase is not essential to understand the limits of the rest of these claims.

Claims 11 to 13, 15, 16, 18, and 20 were rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential steps, such omission amounting to a gap between the steps. Paragraph 13 of the OFFICE ACTION states:

"The omitted steps are: achieving high-performance hardware abstraction. The preambles of claims 11 and 20 state that each claim is a 'method for achieving high-performance hardware abstraction'. However, the method steps for each claim do not refer to a hardware abstraction or achieving high-performance thereof."

The Applicant respectfully submits that the steps of "creating a reference to a logical resource that is associated with a corresponding physical resource," "associating with the logical resource one or more private states" and "executing a plurality of functions that facilitate communication between the physical resource and an algorithm" are the essential steps in "achieving hardware abstraction" as recited in claims 11 and 20. These steps are not insufficient merely because the body of independent claims 11 and 20 fail to recite "achieving hardware abstraction." For example, a claim to a method for baking a cake could recite the steps of: mixing ingredients in a bowl; pouring the mixed ingredients into a cake pan; placing the cake pan in an oven at a predetermined temperature for a predetermined length of time. This example claim is complete without reciting "baking a cake" in the body of the claim. The Applicant submits that independent claims 11 and 20 are likewise complete and include all essential steps.

Paragraph 2 of the OFFICE ACTION stated that claims 1 to 3, 5, 6, 8, 10 to 13, 15, 16, 18 and 20 would be allowable if rewritten to overcome rejections under 35 U.S.C. 112. Accordingly, the Applicant respectfully submits that claims 1 to 3, 5, 6, 8, 10 to 13, 15, 16, 18 and 20 are allowable.

The Applicants respectfully submit that all the present claims are allowable for the reasons set forth above. Therefore early reconsideration and advance to issue are respectfully requested.

If the Examiner has any questions or other correspondence regarding this application, Applicants request that the Examiner contact Applicants' attorney at the below listed telephone number and address to facilitate prosecution.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(972) 917-5290
Fax: (972) 917-4418

Respectfully submitted,

/Robert D. Marshall, Jr./
Robert D. Marshall, Jr.
Reg. No. 28,527